

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

NAKAJYO, Shinsuke et al.

Serial No.: 09/686,958

Filed: October 12, 2000

Group Art Unit: 2824

Examiner: Michael K. Luhrs

Date: December 3, 2003

P.T.O. Confirmation No.: 5708

For. MANUFACTURE OF WAFER LEVEL SEMICONDUCTOR DEVICE AND

SEMICONDUCTOR DEVICE

## **AMENDMENT AFTER FINAL REJECTION**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 20231

Sir:

In response to the Office Action dated September 3, 2003, please amend the above-identified application as set forth below:

Amendments to the Claims begin on page two of this paper.

Remarks begin on page six of this paper.